

FIG. 1

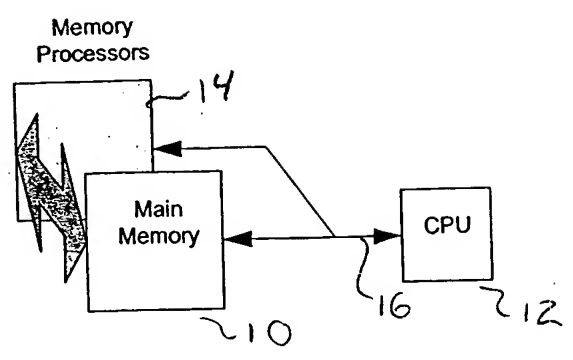
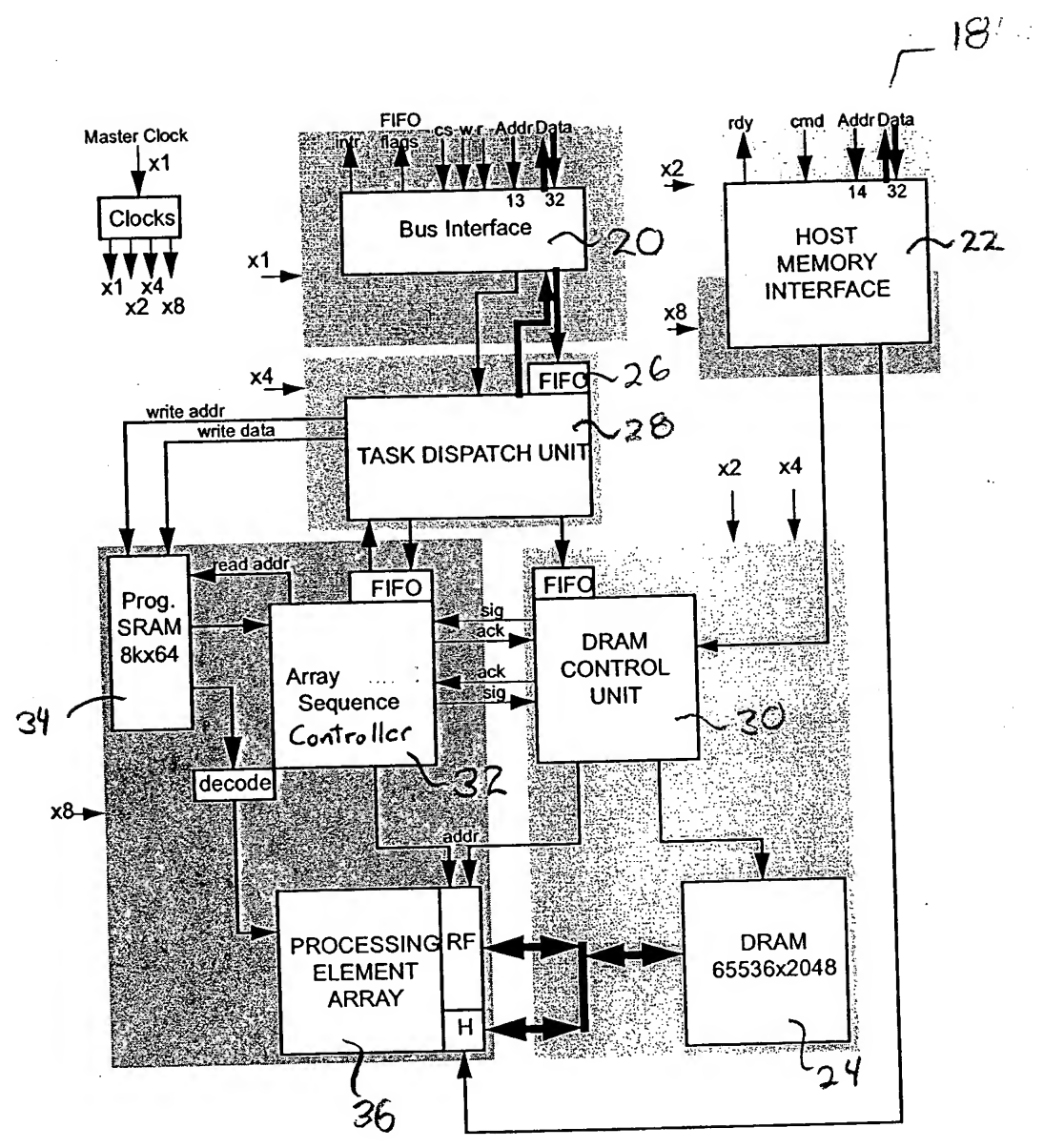


FIG. 2



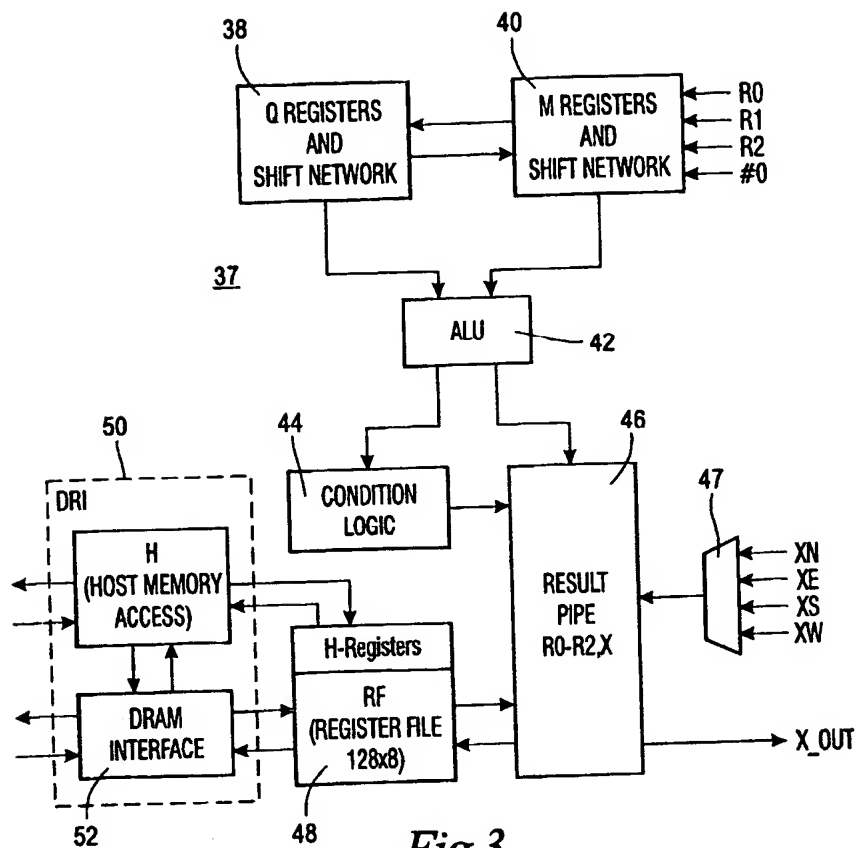


Fig.3

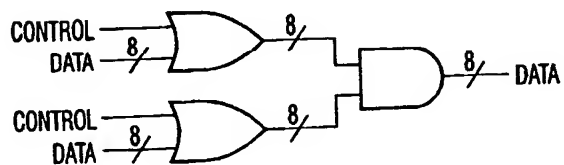
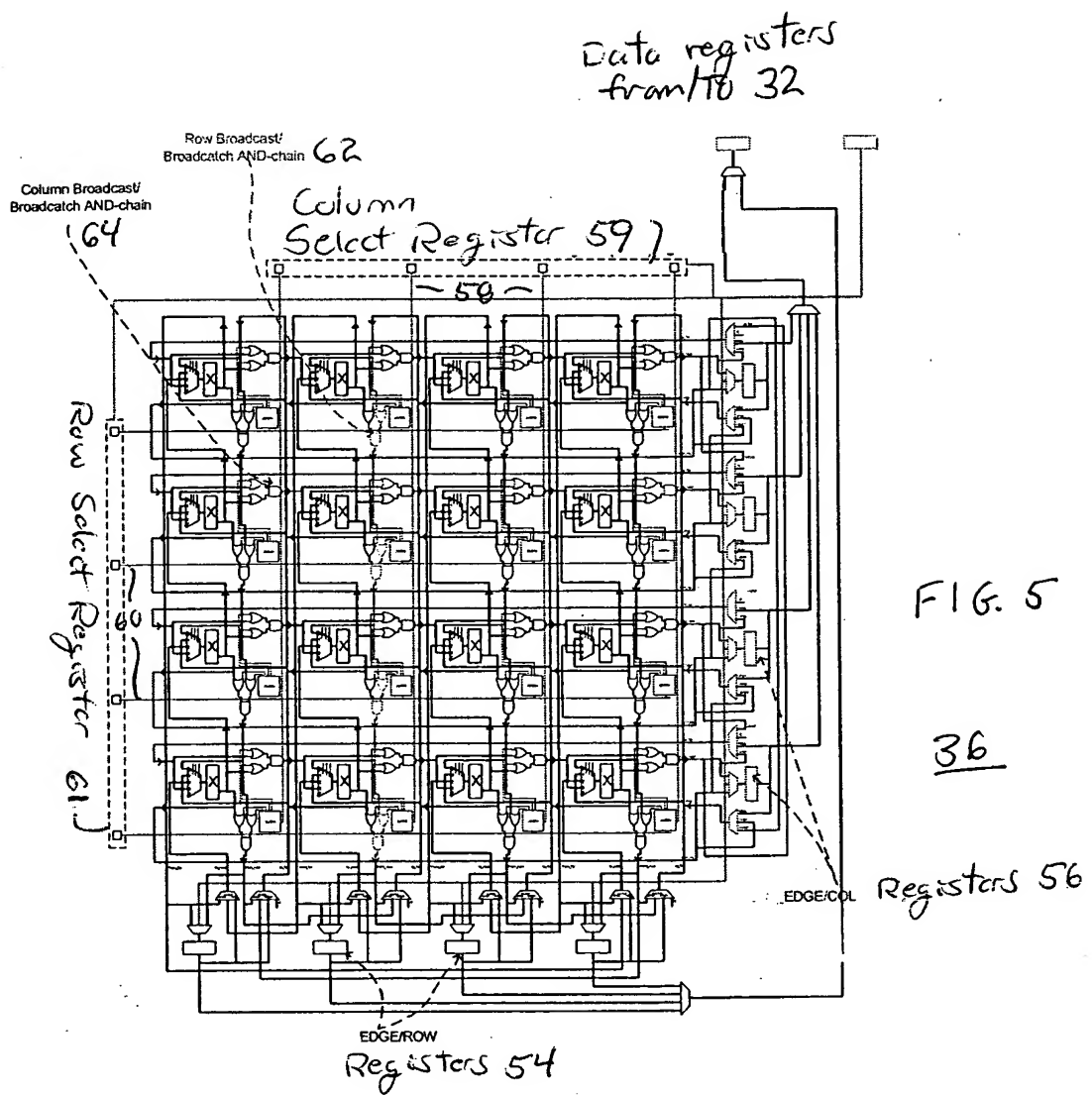
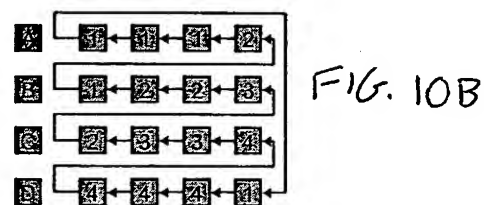
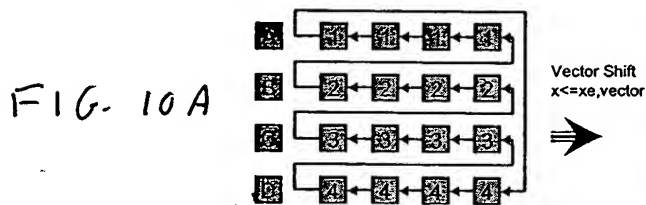
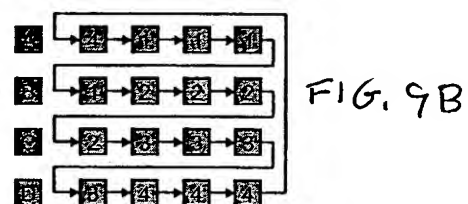
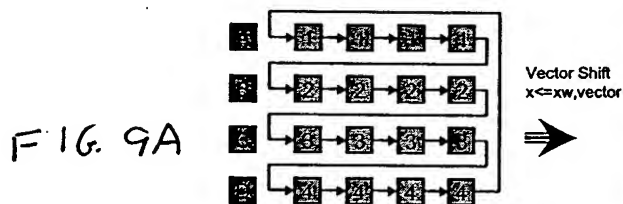
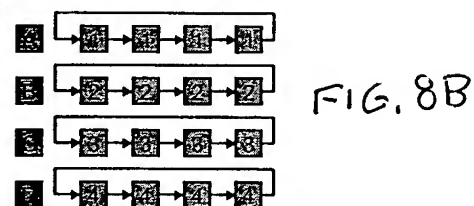
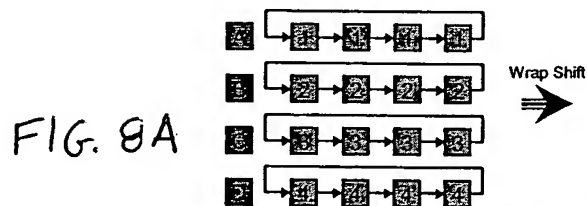
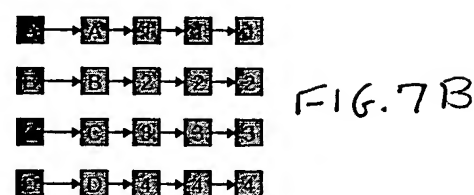
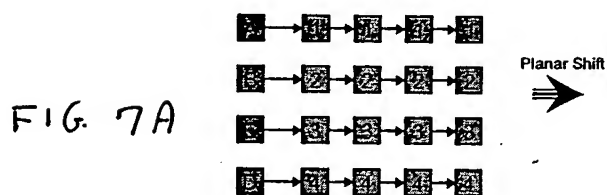
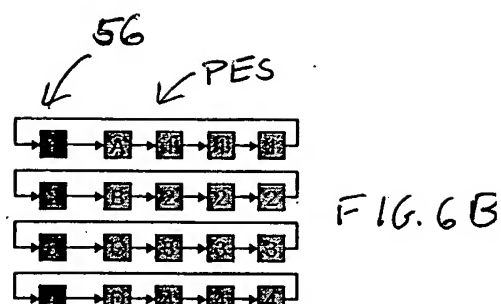
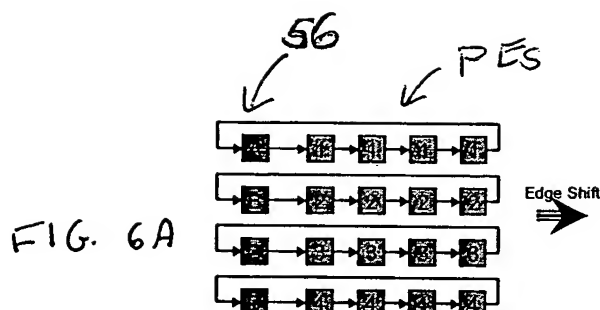


Fig.4





Data is broadcast from edge registers

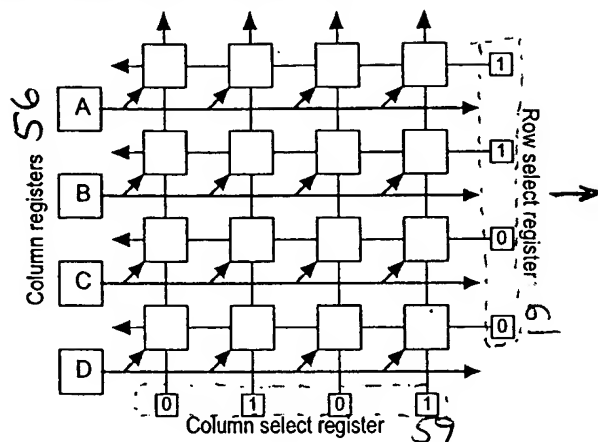


FIG. 11A

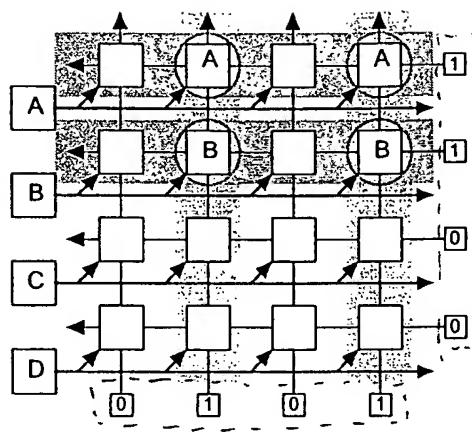


FIG. 11B

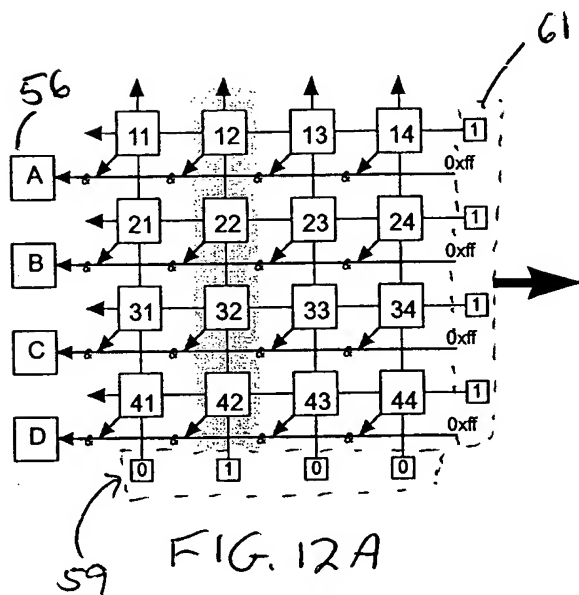


FIG. 12A

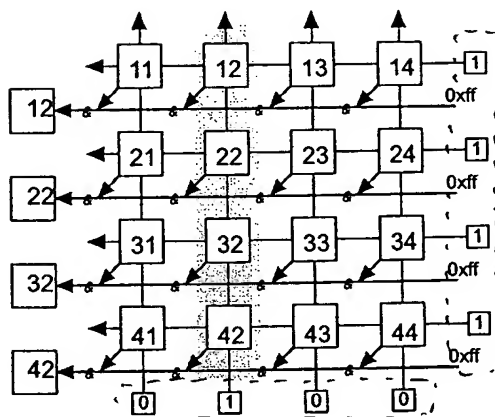


FIG. 12B

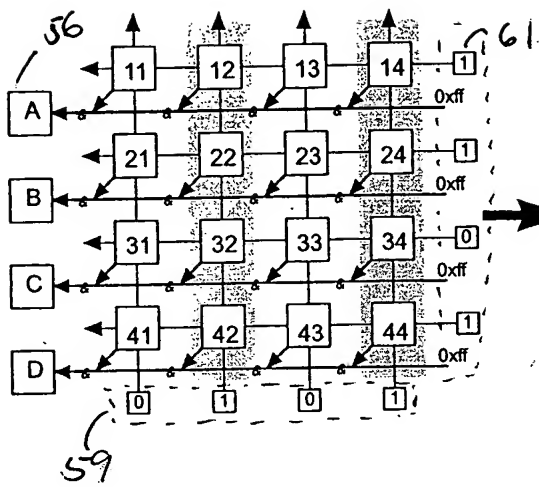


FIG. 13A

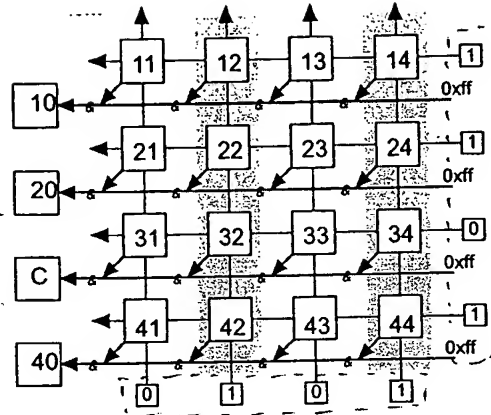


FIG. 13B

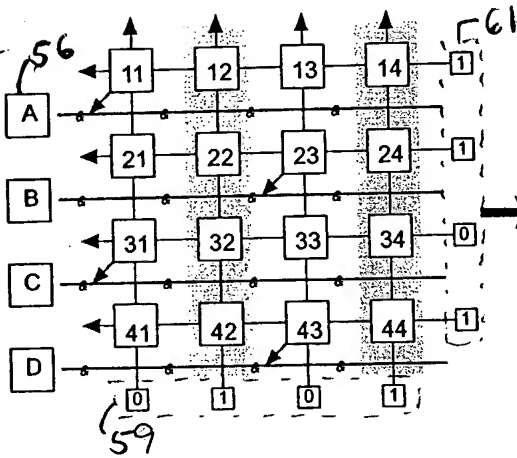


FIG. 14A

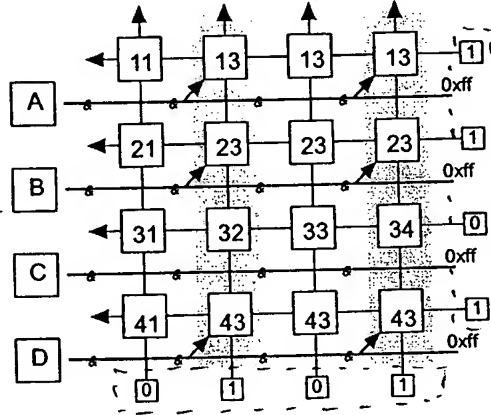


FIG. 14B